

핀번호	명칭	Fpga	IO	설정의미	고정값
33	MODE1	PIN18	17		LOW
34	MODE0	PIN17	16		LOW
42	PWDN1	PIN12	11		HIGH
41	PWDN2	PIN13	12		HIGH
40	PWDN3	PIN14	13		HIGH
39	PWDN4	PIN15	14		HIGH
11	SYNC	PIN5	6		HIGH
20	DOUT1	PIN6	7		입력
19	DOUT2	PIN7	8		입력
18	DOUT3	PIN8	9		입력
17	DOUT4	PIN11	10		입력
27	CLK	PIN2	3		27MHZ
29	DRDY/FSYNC	PIN3	4	ADC START 신호	
28	SCLK	PIN4	5		27MHZ
30	FORMAT2	PIN19	18	오른쪽과 같이 했을때 Frame-Sync Discrete	HIGH
31	FORMAT1	PIN20	19		LWO
32	FORMAT0	PIN21	20		HIGH
10	CLKDIV	PIN16	15		